

# Product Quality Audit Report

<b>Date</b> 20/11/2015	<b>Customer</b> (UNI09) Universitat zu Kiel				
<b>Invotec Details</b>	<b>Tool Number</b> 171442	<b>Works Order</b> 277438	<b>Sales Order</b> 275065	<b>Layer Type</b> MULTILAYER (8)	<b>Release</b> ECSS-Q-ST-70-11C
<b>Customer Details</b>	<b>Part Number</b> STEP FM LVPS		<b>Part Issue</b> 1		<b>Purchase Order Number</b> 441

## PCB MARKINGS

Part number & Issue / revision level correct	Accept	1547
Manufacturers mark & date code correct	Accept	
Release coding (UL mark etc) correct	Accept	

## GENERAL

Profiled edges free from loose fibres, pip marks etc	Accept	<b>Spec:</b>  <b>Actual:</b>
Bow and Twist within specification	Accept	
Materials / surface finish correct	Accept	
General appearance / workmanship	Accept	
Electrical Test complete	Accept	
Laboratory Test complete (Report available if required)	Accept	
Sample board included	Accept	

## OUTER LAYER PATTERN

Annular Ring meets min requirements	Accept
Track width	Accept
Track spacing	Accept
Conductor pattern (Spurious copper etc within acceptable limits)	Accept

## SOLDERMASK / ANNOTATION / PEELABLE MASK

Solder Resist colour / type	N/A
Annotation colour	N/A
No encroachment onto pads (other than by design)	N/A
Solder Resist free from skips, inclusions etc	N/A
Legend clear and legible	N/A
Adhesion	N/A
No contamination of component holes	Accept
Peelable mask - correct type / registration / coverage	N/A

## DIMENSIONS / SCORE / BOARD THICKNESS

Outline dimensions correct	Accept	<b>Spec:</b> 2.40 +0.24 / -0.24 <b>Actual:</b> 2.33
Hole sizes correct	Accept	
Score lines - correct position / web thickness	Accept	
Board thickness - MM	Accept	



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## Hole size summary

## DIMENSIONAL SUMMARY

Hole Ref	Nominal	Tolerance		Actual	Type	Unit	Ref No	Nominal	Tolerance		Actual	Deviation	Unit
		+	-						+	-			
B	0.59	0.10	0.10	0.60	PTH	MM							
C	0.65	0.10	0.10	0.68	PTH	MM							
D	0.70	0.10	0.10	0.75	PTH	MM							
E	0.80	0.10	0.10	0.85	PTH	MM							
F	2.19	0.10	0.10	2.25	PTH	MM							

Dimensional report  
attached

## NOTES

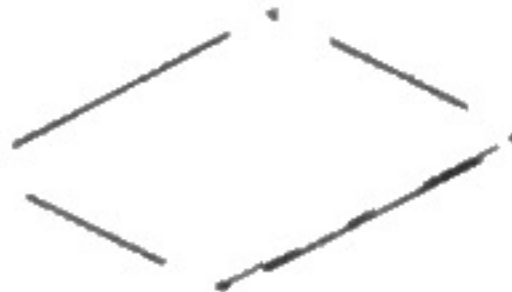
PCB SERIAL NUMBER USED-AA2  
UNABLE TO MEASURE 0.4 PTH AT FINAL STAGE.

## COMPLETED BY

Lisa Ruff



# Amphenol Invotec



Dosthill  
Tamworth  
Staffordshire  
B77 5HH

Drawing No.	STEP FM LVPS	Batch No.		Date	19-Nov-15 2:32
Tool Number	171442	W/O No	277438	Inspector	J.KEMPER-LINES
Customer	UNIVERSITAT	Material		Notes	AA 2

Identifier	Dimension		Dim.	Limit		Pass/Fail	Geometric Tolerance		
	Nominal	Actual		Upper	Lower		Actual	Limit	Pass/Fail
120mm Between Line and Line	120.00	120.02	0.02	120.20	119.80	PASS			
100mm Between Line and Line	100.00	100.01	0.01	100.20	99.80	PASS			
2mm Rad	Unit 5	R2.00	R0.01	R2.20	R1.80	PASS			
2mm Between Circle and Line		2.00	2.03	2.10	1.90	PASS			
2mm Between Line and Circle		2.00	2.01	2.10	1.90	PASS			