

Date 10/08/2015	Customer (UNI09) Universitat zu Kiel				
Invotec Details	Tool Number 168644	Works Order 274067	Sales Order 271877	Layer Type MULTILAYER (6)	Release ECSS-Q-ST-70-11C
Customer Details	Part Number EPT-HET ANALOG BOARD			Part Issue 1	Purchase Order Number 179

PCB MARKINGS					
Part number & Issue / revision level correct	Accept	1532			
Manufacturers mark & date code correct	Accept				
Release coding (UL mark etc) correct	Accept				

GENERAL					
Profiled edges free from loose fibres, pip marks etc	Accept	<div>Spec:</div> <div>Actual:</div>			
Bow and Twist within specification	Accept				
Materials / surface finish correct	Accept				
General appreance / workmanship	Accept				
Electrical Test complete	Accept				
Laboratory Test complete (Report available if required)	Accept				
Sample board included	Accept				

OUTER LAYER PATTERN					
Annular Ring meets min requirements	Accept				
Track width	Accept				
Track spacing	Accept				
Conductor pattern (Spurious copper etc within acceptable limits	Accept				

SOLDERMASK / ANNOTATION / PEELABLE MASK					
Solder Resist colour / type	N/A				
Annotation colour	N/A				
No encroachment onto pads (other than by design)	N/A				
Solder Resist free from skips, inclusions etc	N/A				
Legend clear and legible	N/A				
Adhesion	N/A				
No contamination of component holes	Accept				
Peelable mask - correct type / registration / coverage	N/A				

DIMENSIONS / SCORE / BOARD THICKNESS					
Outline dimensions correct	Accept	<div>Spec: 1.60 +0.16 / -0.16</div> <div>Actual: 1.52</div>			
Hole sizes correct	Accept				
Score lines - correct position / web thicknes	Accept				
Board thickness - MM	Accept				



# Product Quality Audit Report

<b>Date</b> 10/08/2015	<b>Customer</b> (UNIO9) Universitat zu Kiel				
<b>Invotec Details</b>	<b>Tool Number</b> 168644	<b>Works Order</b> 274067	<b>Sales Order</b> 271877	<b>Layer Type</b> MULTILAYER (6)	<b>Release</b> ECSS-Q-ST-70-11C
<b>Customer Details</b>	<b>Part Number</b> EPT-HET ANALOG BOARD			<b>Part Issue</b> 1	<b>Purchase Order Number</b> 179

<b>Hole size summary</b>	<b>DIMENSIONAL SUMMARY</b>
--------------------------	----------------------------

Hole Ref	Nominal	Tolerance +   -		Actual	Type	Unit	Ref No	Nominal	Tolerance +   -		Actual	Deviation	Unit
	0.024	0.002	0.002	0.025	PTH	Inch							
	0.026	0.002	0.002	0.027	PTH	Inch							
	0.028	0.002	0.002	0.030	PTH	Inch							
	0.079	0.002	0.002	0.081	PTH	Inch							
	0.087	0.002	0.002	0.089	PTH	Inch							

**Dimensional report  
attached**

## NOTES

PCB SERIAL NUMBER USED-AM3  
0.016 PTH-UNABLE TO MEASURE AT FINAL STAGE.

## COMPLETED BY

**Lisa Ruff**



# Invotec Circuits Tamworth



Dosthill  
Tamworth  
Staffordshire  
B77 5HH

Drawing No.	EPT-HET ANALOG B	Batch No.	AF 3	Date	07-Aug-15 19:08
Tool Number	168644	W/O No	274067	Inspector	S.WOODFORD
Customer	KIEL	Material		Notes	

Identifier	Dimension		Dim.	Limit		Pass/Fail	Geometric Tolerance		
	Nominal	Actual		Upper	Lower		Actual	Limit	Pass/Fail
120mm	120.00	120.02	0.02	120.10	119.90	PASS			
100mm	100.00	100.00	0.00	100.10	99.90	PASS			
8mm	8.00	7.95	-0.05	8.10	7.90	PASS			
9mm	9.00	9.05	0.05	9.10	8.90	PASS			