

Date	Customer				
27/08/2015	(UNI09) Universitat zu Kiel				
Invotec Details	Tool Number	Works Order	Sales Order	Layer Type	Release
	170127	275502	273316	MULTILAYER (8)	ECSS-Q-ST-70-11C
Customer Details	Part Number		Part Issue	Purchase Order Number	
	EPT-HET LVPS		1-V02	179	

PCB MARKINGS		
Part number & Issue / revision level correct	Accept	
Manufacturers mark & date code correct	Accept	
Release coding (UL mark etc) correct	Accept	

GENERAL		
Profiled edges free from loose fibres, pip marks etc	Accept	
Bow and Twist within specification	Accept	Spec:                      Actual:
Materials / surface finish correct	Accept	
General appreance / workmanship	Accept	
Electrical Test complete	Accept	
Laboratory Test complete (Report available if required)	Accept	
Sample board included	Accept	

OUTER LAYER PATTERN		
Annular Ring meets min requirements	Accept	
Track width	Accept	
Track spacing	Accept	
Conductor pattern (Spurious copper etc within acceptable limits	Accept	

SOLDERMASK / ANNOTATION / PEELABLE MASK		
Solder Resist colour / type	N/A	
Annotation colour	N/A	
No encroachment onto pads (other than by design)	N/A	
Solder Resist free from skips, inclusions etc	N/A	
Legend clear and legible	N/A	
Adhesion	N/A	
No contamination of component holes	Accept	
Peelable mask - correct type / registration / coverage	N/A	

DIMENSIONS / SCORE / BOARD THICKNESS		
Outline dimensions correct	Accept	
Hole sizes correct	Accept	
Score lines - correct position / web thicknes	N/A	
Board thickness - MM	Accept	Spec: 2.40 +0.24 / -0.24                      Actual: 2.36





## Product Quality Audit Report

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### Hole size summary

### DIMENSIONAL SUMMARY

Hole Ref	Nominal	Tolerance		Actual	Type	Unit	Ref No	Nominal	Tolerance		Actual	Deviation	Unit
		+	-						+	-			
5	0.60	0.05	0.05	0.64	PTH	MM							
6	0.65	0.05	0.05	0.69	PTH	MM							
7	0.70	0.05	0.05	0.70	PTH	MM							
8	0.80	0.05	0.05	0.80	PTH	MM							
9	2.00	0.05	0.05	1.98	NPTH	MM							
10	2.20	0.05	0.05	2.20	PTH	MM							

Dimensional report  
attached

### NOTES

PCB serial number measured ag1

### COMPLETED BY

Jessica Groves

# Invotec Circuits Tamworth

Dosthill  
Tamworth  
Staffordshire  
B77 5HH

Drawing No.	EPT-HET-LVPS	Batch No.	AM 1	Date	27-Aug-15 8:50
Tool Number	170127	W/O No	275502	Inspector	S.WOODFORD
Customer	UNIVERSITAT ZU KI	Material		Notes	

Identifier	Dimension		Dim.	Limit		Pass/Fail	Geometric Tolerance		
	Nominal	Actual	Error	Upper	Lower		Actual	Limit	Pass/Fail
120mm	120.00	120.04	0.04	120.30	119.70	PASS			
100mm	100.00	100.01	0.01	100.30	99.70	PASS			
2mm	2.00	1.94	-0.07	2.10	1.90	PASS			
2mm	2.00	1.98	-0.02	2.10	1.90	PASS			
2mm Rad	Unit 10	R2.00	R0.02	R2.10	R1.90	PASS			