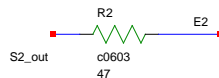


\$Id: RADE.sch,v 1.6 2006/11/07 12:20:04 bottcher Exp \$



refdes=CONN

footprint=RADE



net=Vcc:1
net=Vss:2
net=vfet:3
net=GND:4
net=vbias:5
net=vfet:6
net=Vss:7
net=GND:8
net=E3:9
net=GND:10
net=Vcc:11
net=vbias:12
net=Vcc:13
net=Vss:14
net=vfet:15
net=GND:16
net=vbias:17
net=vfet:18
net=Vss:19
net=GND:20
net=E2:21
net=GND:22
net=Vcc:23
net=vbias:24
net=Vcc:25
net=Vss:26
net=vfet:27
net=GND:28
net=vbias:29
net=vfet:30
net=Vss:31
net=GND:32
net=E1:33
net=GND:34
net=Vcc:35
net=vbias:36
net=GND:37
net=GND:38
net=GND:39

MSL RAD FEE: RADE board interconnects

TITLE \$Id: RADE.sch,v 1.6 2006/11/07 12:20:04 bottcher Exp \$

FILE: \$RCSfile: RADE.sch,v \$

REVISION: \$Revision: 1.6 \$

PAGE 1 OF 1

DRAWN BY: Stephan I Böttcher