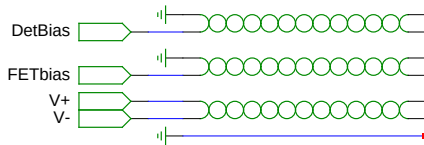
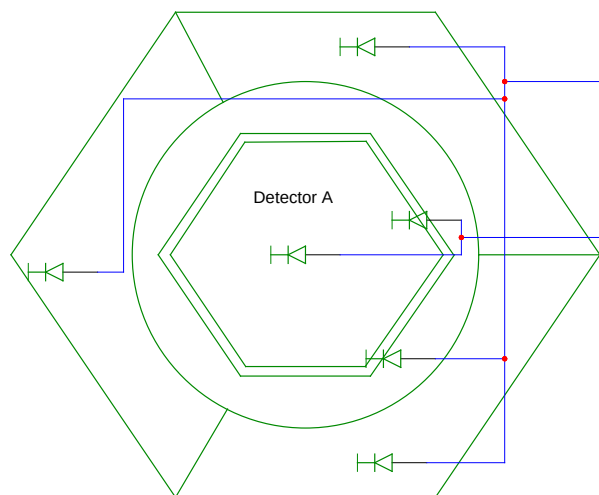


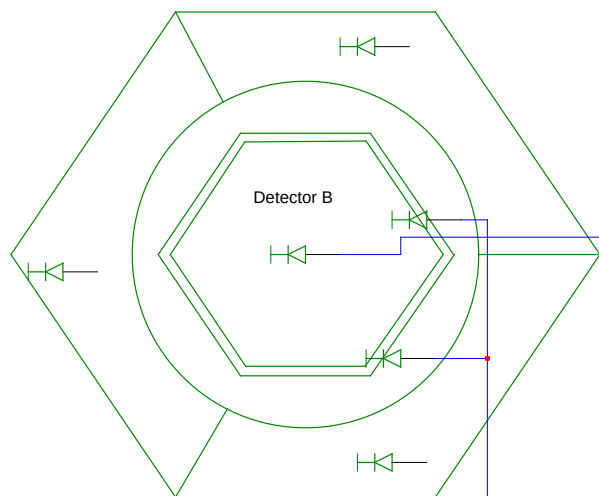
CSA: Charge Sensitive Amplifier
 +: resistive sum network
 SH: SHaper amplifier
 DGSH: Dual Gain SHaper amplifier



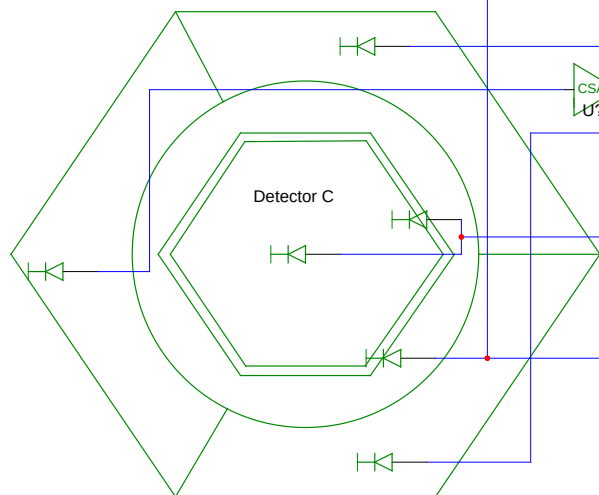
Supply Voltages
 (redundant ?)



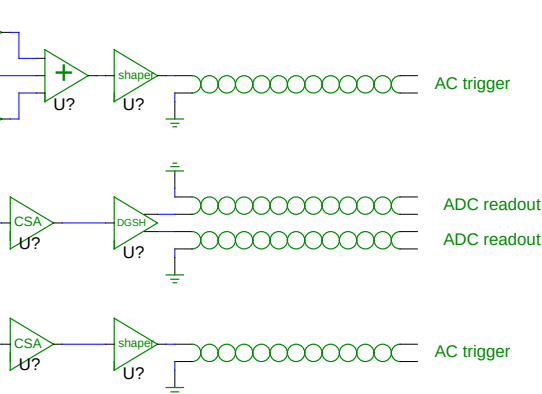
Detector A (top): inner/outer ring for high-resolution channel



Detector B (middle): View definition only



Detector C (bottom):
 Center: telescope
 Middle: Anticoincidence, Crosstalk
 Outer: BC430 anticoincidence readout



MSL RAD telescope front-end circuit diagram

TITLE

FILE: \$RC\$File: telescope.sch.v \$

REVISION: \$Revision: 1.6 \$ \$Date: 2005/11/07 12:12:05 \$

PAGE

OF

DRAWN BY: Stephan B. J. Fletcher