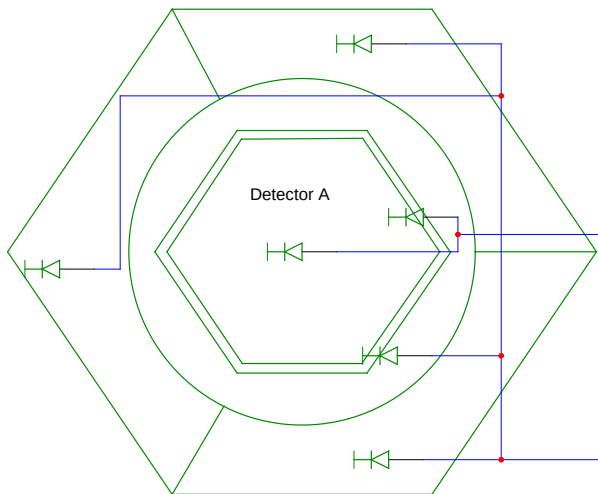
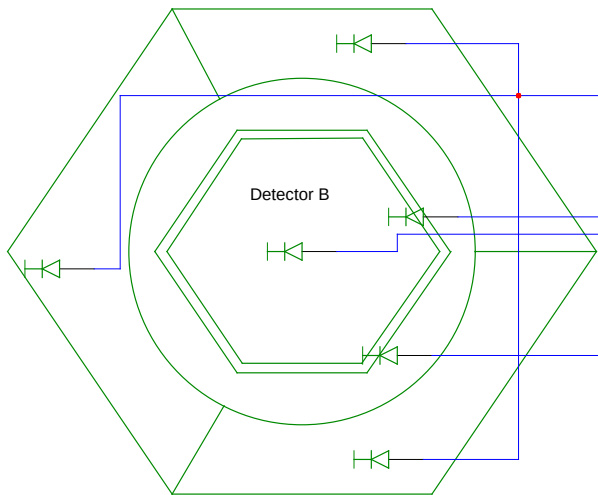
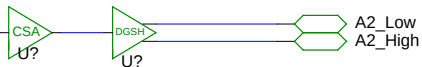
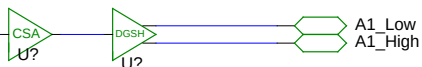


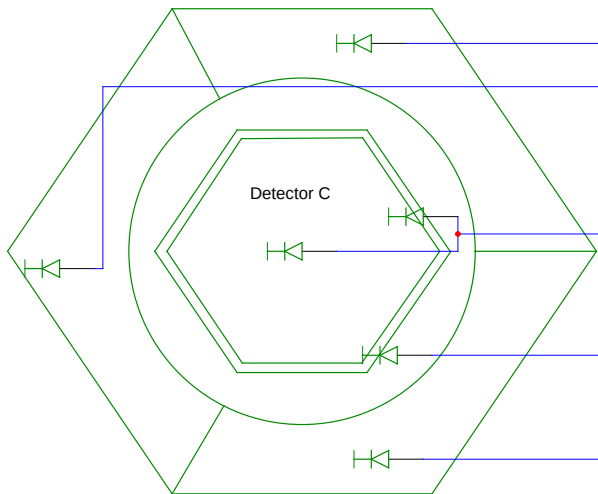
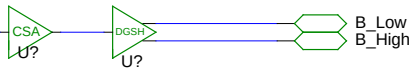
CSA: Charge Sensitive Amplifier
 +: passive sum network
 SH: SHaper amplifier
 DGSH: Dual Gain SHaper amplifier



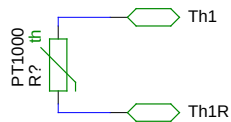
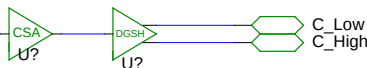
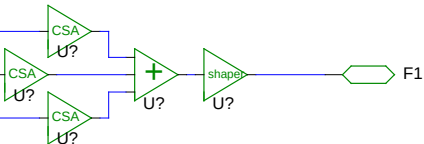
Detector A (top):
 Center: A1 telescope
 Middle + Outer: A2 telescope



Detector B (middle):
 Center: B telescope
 Middle: Anticoincidence
 Outer: Unused (bias only)



Detector C (bottom):
 Center: C telescope
 Middle: Anticoincidence, Crosstalk
 Outer: BC430 anticoincidence readout



MSL RAD telescope front-end circuit diagram

TITLE

FILE: \$PCHFile: telescope.ps, v \$

REVISION: \$Revision: 1.1 \$

DATE: 2008/01/25 15:44:21 \$

DRAWN BY: \$Author: \$

STEPHAN B. TITCHER

PAGE 1 OF 1